Simulation of a Voltage Controlled Resistor Mimicking the Geometry of a MOSFET Device having Graphite Channel



COMSOL

CONFERENCE

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• Outline :

- Introduction
- Geometry of device
- Application COMSOL multiphysics
- Results
- Summary



Introduction :

• Voltage Control Resistor (VCR) :

A device where the resistance or conductance depends upon external voltage.

• The device has **3 terminals**.

• Geometry is **similar to MOSFET** device.



Geometry of the Device :



 V_g (gate voltage) can be termed as control voltage.

 V_d (drain voltage) can be termed as applied voltage.



Governing Equations :

$$\nabla \cdot (\varepsilon_r \nabla V) = q(n - p + N_A^- - N_D^+)$$





Continuity eqns.

$$J_n = (\mu_n \nabla E_c + \frac{qD_{n,th}}{T_I} \nabla T_I)n + \mu_n k_B T_I G(n / N_C) \nabla n$$



$$U_p = (\mu_p \nabla E_c + \frac{qD_{p,th}}{T_c} \nabla T_I) p + \mu_p k_B T_I G(n / N_V) \nabla p$$

Fermi-dirac carrier statistics

MULTIPHYSICS

• Assumption :

• Insulation at the boundaries.



• Ohmic metal contact.



• All the terminals are voltage terminal.



Application COMSOL Multiphysics :

- Module: Semiconductor.
- Method: Finite volume.
- **Discretization:** Scharfetter gummel.
- Carrier Statistics: Fermi-dirac.
- Mesh: Triangular mesh with 29429 domain elements.
- Study: Stationary.





Parameters that Effects Channel Current :

• Dielectric constant of insulator layer (\mathcal{E}_r) .

• Thickness of insulator (d_{ins}) .

Effect of Si nanoparticle loading.





Drain

Va

Metal Contact

Gate

Insulator





Surface Potential and Current

Density:







• At Channel Region :





• Summary :

- Channel is made of graphite.
- Current through the channel depends on the external control voltage (V_g) .
- Channel current depends on d_{ins} and Si nanoparticle loading but depends very less on \mathcal{E}_r .



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• References :

- 1. N. Kurra and G.U. Kulkarni, Pencil-on-paper: electronic devices. Lab on a Chip, 13(15): p. 2866-2873(2013).
- 2. E. Fortunato, et al. Paper field effect transistor. (2009).
- 3. N. Kurra, D. Dutta, and G.U. Kulkarni, Field effect transistors and RC filters from pencil-trace on paper. *Physical Chemistry Chemical Physics*,**15(21)**:p. 8367-8372(2013).
- 4. R. Matsumoto, Y. Hoshina, and N. Akuzawa, Thermoelectric Properties and Electrical Transport of Graphite Intercalation Compounds. *Materials Transactions*, **50(7)**:p. 1607 1611(2009).
- 5. A. Russo, et al., Pen-on-Paper Flexible Electronics. Advanced Materials, 23(30):p. 3426-3430(2011).
- V.V. Brus and P.D. Maryanchuk, Graphite traces on water surface A step toward low-cost pencil-on-semiconductor electronics and optoelectronics.Carbon,78(0):p. 613-616(2014).
- 7. B. Standley, et al., Graphene–Graphite Oxide Field-Effect Transistors. Nano Letters, 12(3): p. 1165-1169(2012).











